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Box PATENT APPLICATION

Assistant Commissioner for Patents Re: New U.S. Patent Appln.
Washington, D.C. 20231 Our Ref: 381NP/47598

Sir:

Transmitted herewith for filing is the patent application of:

Fumio NARISAWA; Hidemitsu NAYA; Takanori YOKOYAMA and Keiichiro OHKAWA

entitled: **OBJECT-ORIENTED OPTIMIZATION CODE GENERATOR AND A METHOD THEREFOR**

Enclosed are:


1. Specification, including 10 claims (26 pages).
2. 13 Sheets of X Formal Informal drawings showing Figs. 1 - 17.
3. X Declaration and Power of Attorney (**executed**).
4. Assignment of the invention to Hitachi Ltd.
5. Certified copy of Priority Document 10 - 038329 filed in Japan on February 20, 1998, the priority of which is being claimed under 35 U.S.C. §119 and 37 C.F.R. §1.55.
6. X Information Disclosure Statement.
7. The filing fee has been calculated as shown below:

Basic Fee				\$380/760 =	\$760.00
Total Claims	<u>10</u>	- 20 =	<u>0</u>	x \$ 9/18 =	\$
Independent Claims	<u>4</u>	- 3 =	<u>1</u>	x \$39/78 =	\$ 78.00
Multiple Dependent Claim Presented				\$133/260 =	\$
Total Filing Fee					<u>\$838.00</u>

A check in the amount of \$ 878.00 for X filing fee X recording fee is enclosed.

The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment, to Deposit Account No. 05-1323 (Docket #381NP/47598). A duplicate copy of this sheet is enclosed.

Respectfully submitted,


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OBJECT-ORIENTED OPTIMIZATION CODE GENERATOR AND A METHOD
THEREFOR

BACKGROUND OF THE INVENTION

5 The present invention relates to an automatic software generating system, and in particular, to a software generating system for automatically generating a software from an object-oriented specification which features an improved code efficiency.

10 A code generating system generates a software from an object-oriented software statement. Generally, a specification described by an operator is entered via an input device, then the specification is analyzed, and a program code is generated and output.

15 However, when a code is generated simply on the basis of the specification described by the operator, there arises such a problem that a target code becomes enormously large, thereby requiring a large memory capacity therefor.

20 In order to solve this problem associated with the conventional art, JPA Laid-Open No. 6-266562 discloses a method for reducing the size of a target code. This method includes the steps of: comparing a class name to which its method belongs and an input file if they coincide with each
25 other during semantic analysis processing of a class declaration in a context semantic analyzer provided for analyzing the specification written by the operator;

generating in an output code an instance and declaration in a method address store table upon matching therebetween; and generating in the output code only the declaration in the method address store table upon non-matching therebetween.

5 However, the conventional code generation method as described above does not take into account whatsoever anything about an object-oriented function to use for generating an object-oriented program code.

10 That is, when generating a code from an object-oriented specification, unnecessary codes are also generated for unused functions of the object-oriented functions.

15 When this conventional code generation method is applied, in particular, to a software for an embedded control system, because a resulting output code becomes enormous, a large sized unit having a large memory capacity capable of implementing the enormous code will be required, thus there is such a problem that the cost of manufacture will increase substantially.

20

SUMMARY OF THE INVENTION

25 The present invention is contemplated to solve the problems associated with the prior art, and provide for a code generating system in which its code is optimized to suit for any particular target of application and to be applicable to any embedded control system without need of increasing its memory capacity.

The above-mentioned object of the invention can be accomplished by provision of a code generating system which is comprised of: a specification analyzer means which analyzes an object-oriented specification, for deriving
5 specification information; a function removal means for removing unnecessary functions out of plural object-oriented functions according to a predetermined function removal rule on the basis of the specification information derived from the specification analyzer means, and
10 producing a program information without using the unnecessary functions; and a code generation means for generating a code on the basis of the program information produced by the function removal means.

According to the invention, an unnecessary function
15 in an object-oriented programming language is removed during the generation of a code from an object-oriented specification, thereby eliminating its mechanism itself required for realizing the unnecessary function. As a result, the code which can substantially reduce its memory
20 capacity can be generated.

BRIEF DESCRIPTION OF THE DRAWINGS

Other objects, features and advantages of the invention will be apparent from the following description
25 taken in connection with the accompanying drawing wherein:

Fig. 1 is a schematic block diagram indicating a code generating system of the invention;

Fig. 2 is a block diagram indicating operation sequences of a total controller in a processing unit of the invention;

Fig. 3 shows an example of model diagrams and its
5 inputs;

Fig. 4 shows an example of operation diagrams and its inputs;

Fig. 5 is an example of function select items displayed;

10 Fig. 6 shows operation sequences in a specification analyzing unit;

Fig. 7 shows operation sequences in an object-oriented function removal unit;

Fig. 8 shows operation sequences in a code
15 generator unit;

Figs. 9(a) and (b) show results of respective code generations;

Figs. 10(a) and (b) show results of respective execution codes generated;

20 Figs. 11(a) and (b) show respective memory allocations in case a virtual function is executed or not;

Fig. 12 shows operations of the total controller unit;

Fig. 13 shows an example of the function select
25 items displayed;

Fig. 14 shows a table of the function select items;

Fig. 15 shows a configuration of an input screen

controller;

Fig. 16 shows the operation of the total control unit; and

Figs. 17(a) and (b) show examples of displays,
5 respectively.

PREFERRED EMBODIMENTS

Figure 1 is a schematic block diagram indicating a configuration of a code generator system of one embodiment of the invention. The code generator system is comprised
10 of: an input device 101 such as a keyboard and a mouse for inputting a software specification and function select items which define conditions for generating an optimized code; a display device 102 such as a CRT for displaying the
15 software specification and the function select items having been input through the keyboard and the like of the input device 101, as well as a code generated; a read/write device 103 for reading a software specification and function select items from a portable memory device such as a floppy
20 disk, and writing the code generated into a memory device; a memory 104 for storing the software specification and the function select items having been entered through the input device 101 or the read/write device 103; and a processor unit 105 for generating an optimized code on the basis of
25 the software specification and the function select items transmitted from the input device 101 and memory device 104.

Further, the processing unit 105 is comprised of: a

specification analyzer section 106 for executing lexical and grammatical analyses of the software specification entered; an object-oriented function removal section 107 for removing unnecessary functions on the basis of the

5 function select items entered; a code generator section 108 for generating a code on the basis of the software specification written with the unnecessary functions removed by the object-oriented function removal section 107 and undergone the lexical and grammatical analyses in the

10 specification analysis section 106; an input display control section 109 for controlling the display screen of the display device 102 so as to enable the software specification and function select items to be input from the input device 101; and a total control section 110 for

15 controlling respective sections. Still further, the input display control section 109 includes a class interface description section 111; a process detail description section 112; and a function select item description section 113. In addition, the code which is generated in the code

20 generator section 108 is displayed on the display device 102, and/or stored in a portable memory device such as a floppy disk via the read/write device 103.

With reference to Fig. 2, operation sequences of the total control unit 110 in the processor unit 105 are

25 shown in a flow chart. This processor unit 105 is provided with an input mode for inputting a software specification and function select items for code generation, and a code

generation mode for generating a code on the basis of the software specification entered. When the input mode is selected via the input device 101 by the operator, the total control section 110 starts the input display control section 109. Then, the input display control section 109 accepts a model diagram representing a pattern of a software specification entered by the operator via the class interface description section 111, and stores this model diagram in the memory device 104.

10 Then, the process detail description section 112 within the input display control unit 109 is started to allows for the operator to accept the input of an operational diagram representing the pattern of the software specification, then this operational diagram is
15 stored in the memory device 104.

 Finally, the function select item description section 113 is started to allow for entry of a function select item which defines conditions for generating a code on the basis of the software specification entered, and to
20 store the same in the memory device 104.

 On the other hand, in case the code generation mode is selected by the operator, the total control section 110 allows to read a model diagram and operational diagram which is the software specification from the memory device
25 104, then starts the specification analyzer section 106 to execute lexical and grammatical analyses thereof. Then, the object-oriented function removal section 107 is started to

determine which function is not needed in accordance with the function select items stored in the memory device 104. Then, the code generator section 108 is started to generate a code on the bases of the software specification subjected

5 to the lexical and grammatical analyses in the specification analyzer section 106, and of an output code pattern determined in the object-oriented function removal section 107. Further, the code generated is displayed on the display device 102 upon request by the operator, and/or

10 written into a floppy disk via the read/write unit 103.

Each processing described above will be set forth in detail using a method referred to as OOIE in the object-oriented analysis and design work in the following.

A case where the input mode is selected by the

15 operator will be described. Upon selection of the input mode, the total control section 110 starts the class interface description section 111. Thereby, the operator is allowed to input a model pattern shown in Fig. 3 as a software specification using the keyboard and mouse of the

20 input device 101. The model pattern in Fig. 3 indicates processing of an input from a sensor which constitutes, for example, a part of an automobile engine control.

The model diagram indicated in Fig. 3 depicts a structure of objects each constituting a part of a program

25 in an object-oriented programming. By way of example, the model diagram depicted here is referred to also as a class diagram or object diagram in the field of the object-

oriented art.

Static information is described in detail in this model diagram which includes three rows of lines. The first row 301 depicts a name and type of a variable corresponding to a class title, the second row 302 depicts a definition of its data structure, and the third row 303 depicts a title of processing to be executed and a type of argument, respectively.

According to the model diagram of Fig. 3, the title of its class is "Sensor" as indicated in the first row 301. Data of its class as indicated in the second row 302 are "A/D_VALUE", "VALUE" and "PREV_VALUE", wherein the "A/D_VALUE" is defined to be a type of "unsigned char", the "VALUE" to be of "signed short", and the "PREV_VALUE" to be of "signed short", respectively. Further, a method which indicates its process to execute is indicated in the third row 303 as "CHECK()" and "UPDATE()", wherein the "CHECK()" has no argument and a type of its return value is "boolean: void", and wherein the "UPDATE()" has no argument nor a return value. Upon completion of entry of the model diagram described by the operator as stated above, the class interface description section 111 allows the model diagram to be stored in the memory device 104.

Then, the total control section 110 causes the process detail description section 112 to be started to allow for the operator to enter an operational diagram as shown in Fig. 4 using the mouse, keyboard or the like. The

operational diagram refers to a detailed description of a method indicating a process of each object belonging to each class. This may be referred to also as a control chart, a state transition diagram or the like other than the operational diagram in the object-oriented field. In Fig. 4, processing of signals from the sensor as indicated in the third row of Fig. 3 is described in detail. In step 401 of "SENSOR CHECK()", it is determined whether or not an A/D conversion value entered from the sensor satisfies a predetermined condition. If it satisfies (YES), "SENSOR UPDATE ()" is executed, and if does not (NO), a process of step 403 is executed to hold such that "VALUE=PREV_VAL". The operational diagram entered by the operator is then stored in the memory device 104.

Then, the total control section 110 starts the function select item description section 113 to display the function select input screen on the output device 102. The function select refers to a selection of operation whether to use or not to use an object-oriented function such as "dynamic generation of an instance", "virtual function" and the like during its code generation.

Fig. 5 shows an example of input display screens of the function select items. This input screen shows a case where a function select item of "Dynamic selection of an instance" is selected by the operator for an object-oriented task. This function select item screen includes four rows of an input pattern 501, a function item to use

502, a set-up option 503, and an output code pattern 504. The input pattern row 501 is for entering an object name and a method name of an object program. The function item to use 502 is a row for indicating the function of the object program. The set-up option 503 is for choosing between "to use"/"not to use" of the function indicated in the use-of-function item 502. The output code pattern 504 corresponds to respective options in the set-up option 503, and indicates respective output code patterns thereof.

- 10 Namely, a generation pattern information of "OBJNAME, METHODNAME(ARG)" corresponds to "to use" in the set-up option 503, and a generation pattern information of "OBJNAME__METHODNAME(ARG)" corresponds to "not to use" in the set-up option 503, respectively. Further, the
- 15 "OBJNAME_METHODNAME(ARG)" depicts a part's name, which means that a code is generated without using "Dynamic generation of instance" of the object-oriented programming language.

The use-of-function select item 502 and the output code pattern 504 are entered in advance on this function select item screen. Therefore, the operator can easily choose whether to use or not to use its object-oriented function by entering data through the input pattern 501 and set-up option 503. The function select item determined by the operator is stored in the memory device 104.

In the input mode as described hereinabove, the software specification and the function select item are

entered by the operator.

Then, a case where the operator chooses the code generation mode will be described in detail in the following.

5 Upon selection of the code generation mode by the operator, the total control section 110 starts the specification analyzer unit 106.

Lexical and context analyses are executed in the specification analyzer unit 106, then character strings
10 derived from the software specification information having been entered as diagrammatic information and types of these character strings are output as shown in Figs. 3 and 4.

Fig. 6 shows a flow chart of processing for the specification analysis section 106 to carry out. In step
15 601, the specification analysis section 106 reads out the software specification entered by the operator and stored in the memory device 104. In step 602, the software specification information which is entered as the diagrammatic information is checked if it matches with a
20 specification description grammar stored in advance in the memory device 104. As to this matching, "lex" and "yacc" described in the "lex & yacc programming" co-authored by John R. Levine/Tony Mason/Doug Brown, June 21, 1996 were used to determine whether or not a specification
25 description grammar described in the BNF or the like and the software specification having been entered accord with each other. When it is judged as a result of determination

that there exists no matching with the specification description grammar, an "error" is output in step 604 to the display device 102, and the process ends. When it is judged that there exists matching with the specification

5 description grammar, a character string from the software specification entered is output in combination with the name of a variable of this character string as a pair in step 603.

For example, information on the software

10 specification entered by the operator and checked as indicated in Fig. 6 is output sequentially as a character string "sensor", variable name "variable 1", character string "A/D_VALUE", variable name "variable 2", character string "VALUE", and variable name "variable 3".

15 Further, information on the software specification indicated in a rounded square in Fig. 4 is defined to be a "method call", and those indicated in a square are defined to be a "command" in advance, respectively. Thereby, for example, the rounded square 401 is output with its

20 attribute as "method call", its object name as "sensor", its method name as "CHECK", and with no argument.

Then, the total control unit 110 starts object-oriented function removal section 107.

The object-oriented function removal section 107

25 outputs a code pattern according to the function select items entered by the operator and stored in the memory device 104.

A flow chart of processing in the object-oriented function removal section 107 is shown in Fig. 7.

In step 701, a result of grammatical analysis output from the specification analysis section 106 is entered. Then, in step 702, function select items which
 5 define a condition under which a code is to be generated are read from the memory device 104 according to the object name, method name derived from the result of the grammatical analysis, and the input pattern of the function select
 10 items.

In case a table cannot be read from the memory device 104 in step 702, a standard generation pattern information is output in step 704. In case the table could have been read from the memory device 104 in step 702, it
 15 is determined in step 705 whether to use or not to use any particular function according to the function select item having been read. When it is determined to use, a standard generation pattern information defined by the function select item is output in step 707. When judged not to use,
 20 a code generation pattern information defined by the function select item is output in step 706.

Then, the total control section 110 starts code generation section 108 to execute code generation.

A flow chart indicative of operation of the code
 25 generation section 108 is shown in Fig. 8.

In step, 801, the generation pattern information output from the object-oriented function removal unit 107

is entered. Then, in step 802, a code generation is executed on the basis of the aforementioned generation pattern information and the code generation pattern stored and read from the memory device 104.

- 5 By way of example, when the software specification from the specification analysis section 106, and the generation pattern information of "OBJENAME, METHODNAME()" from the object-oriented function removal section 107 as shown in Figs. 3 and 4 are entered, the code generation
- 10 section 108 generates a code on the basis of the code generation pattern information from the memory device 104.

Namely, the memory device 104 stores the following information as a code generation pattern.

```

"class" class name "("
=
15   type 1, data 1;
      type 2, data 2;
      ,
      ,
      ,
20   return value type 1, method 1(argument type 1);
      return value type 2, method 2(argument type 2);
      ,
      ,
      ,
25   ")"
      * check sentence
      "if ("CHECK")><"
  
```

```

execution sentence when the condition is satisfied;
"} else {"
execution sentence when the condition is not satisfied;
"}".

```

5

When use of "dynamic generation of an instance" is not set up in the table, a code shown in Fig. 9(a) is generated. By compiling this resulting code, an execution code as shown in Fig. 10(a) is obtained. This execution code is output to the display device 102, and/or stored in a floppy disk via the read/write device 103.

By way of example, Fig. 9(b) and Fig. 10(b) show a code generated and its compiled execution code when use of "dynamic generation of an instance" is set up in the table.

In comparison of these two execution codes, when the dynamic generation function is used, an instance is explicitly expressed for an explicit designation of its instance in block 1001 in Fig. 10(b), and a code for storing an address of the instance in stack is generated.

In contrast, when the dynamic generation function is not used, there is no area corresponding to the block 1001 for designating the instance, and no code corresponding thereto is output. By removing the object-oriented function in the manner as described hereinabove, the code size can be reduced substantially.

Another case of using a different function as its

object-oriented function will be described by way of example of a "virtual function".

The virtual function sets forth a common interface in a parent class when a plurality of children classes are produced from the parent class using "inheritance", and wherein implementation in each of the plurality of children classes differs from each other. By way of example, the "inheritance" refers to a method of producing a new class by succeeding all the attributes already in existence in some existing class, and describing a difference therebetween.

Fig. 11(a) shows a memory allocation for the code in an execution format, which is generated by the aforementioned method which is set up by choosing the "use of the virtual function" by the operator when the method to use the virtual function is designated. As shown in this drawing, code 1101 for function "FUNC. CHECK()" is stored in a program code section, and a region 1102 for an internal variable, and a table 1103 for calling a virtual function "FUN. CHEKC() CALL_UP TABLE" are stored in a data section. Virtual function table 1101 set forth in the data section realizes a mechanism of the virtual function.

Fig. 11(b) shows a memory allocation of a code compiled in an execution format wherein the function of the virtual function is set "not to use". In this case, because the virtual function is called up directly, a portion corresponding to the table 1103 indicated in Fig. 11(a) is

eliminated.

As described above, by removing some of the object-oriented functions such as dynamic generations of instances and/or virtual functions, a corresponding mechanism which becomes unnecessary for realizing its object-oriented function can be removed from its inclusion. For such purpose, no particular knowledge on each object-oriented mechanism is required for the operator to have, thereby simply designating "use/not use" on the display, optimization of the system can be accomplished.

Another method of designating the function select items will be described in the following.

Fig. 12 indicates control processing of the total control section 110 with respect to the specification analysis section 106 and the object-oriented function removing section 107 shown in Fig. 1. When an object name and a method name are obtained in the specification analysis section 106, the function select item description unit 113 is started so as to allow entry of a function select item corresponding to the object name and the method name obtained. By provision of such arrangement, there is no need for the operator to enter on the entry pattern of the function select items. Therefore, it is only required to display for the operator such a function select item as shown in Fig. 13.

The function select item shown in Fig. 13 is

comprised of a function column and a set-up column, wherein the function column displays a list of object-oriented functions so as to allow for the operator selectively to set up whether or not to use its function corresponding thereto in the set-up column. Then, upon entry by the operator, the function select description unit 113 generates a table as shown in Fig. 14, which is then stored in the memory device 104. Further, code generation patterns corresponding to the "use" and "not use" of each function are prestored in memory device 104.

Then, with reference to a table generated, the object-oriented function removing section 107 reads out a code generation pattern corresponding thereto, and outputs the same to the code generation unit 108 whereby to execute the code generation.

Further example of designating another select item will be described in the following.

Fig. 15 shows a modified arrangement of the input screen control unit 109 of Fig. 1 with addition as new components of an optimization information deriving unit 1501 and an analysis result display unit 1502.

Fig. 16 shows a flow of operation of the total control unit 110 with respect to the specification analysis section 106 and the object-oriented function removing section 107 with the arrangement of Fig. 15. As indicated in Fig. 16, information useful for function designation is derived in the optimization information deriving unit 1501

from the information obtained by the specification analysis section 106, and is displayed on the analysis result display unit 1502 to allow for the operator to designate a function to use on the basis of the information displayed through the function select item description unit 113. The optimization information deriving unit 1501 derives optimization information, for example, by counting the number of instances obtained as the result of analysis in the specification analysis section 106, and identifying "use" or "no use" of the virtual functions. This optimization information derived is displayed on the display device 102 via analysis result display section 1502.

Fig. 17(a) shows an example of display on the analysis result display section 1501. Here, the optimization information including the number of instances, the virtual functions selected and the like derived in the optimization information deriving unit 1501 is displayed.

After this optimization information is displayed, the function select item description section 110 displays function select items as indicated in Fig. 17(b) to prompt the operator to enter "use" or "not use" of the corresponding function.

By allowing the operator to utilize the result of analysis obtained in the specification analysis section 106 as described above, a more effective set-up procedure becomes possible in setting up of the items by the user.

For example, because the number of instances to be utilized in programming is a major factor in making decision whether to use the dynamic generation of instances or not, such information will help the operator set up more

5 effectively.

Further, this embodiment of the invention is not limited in its applicability to any specific programming language, and can operate in the same way using statements described in C++ or Java.

10

What is claimed is:

1. A software generation system comprising:

a specification analysis means which analyzes an
object-oriented specification for deriving specification
5 information;

a function removing means which checks said
specification information derived by said specification
analysis means by collating with a function removal rule
which is predetermined, and removes a function which
10 becomes unnecessary from a set of object-oriented
functions, for generating program information excluding the
unnecessary function; and

a code generation means for generating a code
according to said program information obtained by said
15 function removing means.

2. A software generation system according to claim 1,
wherein said function removing means removes a function of
a virtual function according to said function removing rule.

3. A software generation system according to claim 1,
wherein said function removing means removes a function of
dynamic generation (or installation) of an instance
according to said function removing rule.

4. A software generation system according to claim 1,
wherein said function removing rule comprises:

an input pattern including an object name and a method name;

a pattern which indicates an object-oriented function, and whether "to use" or "not to use" said

5 object-oriented function; and

an output pattern including output code generation patterns with said object-oriented function being used and not used.

10 5. A software generation system comprising:

an input means for inputting a specification described as diagrammatic information, and selecting an object-oriented function to utilize;

an analysis means for analyzing said specification entered via said input means;

15 a function selection means which outputs pattern information for use in generating a code on the basis of a result of analysis by said analysis means and according to said object-oriented function selected; and

20 a code generation means for generating a program code of said specification analyzed according to the pattern information output from said function selection means.

25 6. A software generation system according to claim 5, wherein said function selection means selects exclusively a function of a virtual function, and said code generation

means generates a code using the function of said virtual function exclusively selected.

7. A software generation system according to claim 5,
5 wherein said function selection means selects exclusively a function of dynamic installation of an instance, and said code generation means generates a code using the function of dynamic installation of said instance exclusively selected.

10

8. A software generation system comprising:
a specification analysis means which analyzes an object-oriented specification for deriving specification information;

15 an analysis result display means for displaying a status of use of an object-oriented function from said specification information;

an input means whereby to select an object-oriented function to utilize;

20 a function memory means for storing a function selected via said input means;

a program information generation means for generating program information on the basis of said specification information derived by said specification

25 analysis means and using said function selected and stored in said memory means; and

a code generation means for generating a code on

the basis of said program information obtained by said program generation means.

9. A software generation system according to claim 8,
5 wherein said analysis result display means displays a method of a plurality of process methods which is not used.
10. A software generation method comprising the steps of:
- 10 analyzing an object-oriented specification entered;
generating program information using object-oriented functions without unnecessary functions according to a predetermined function removing rule; and
generating a code of said specification analyzed on
15 the basis of said program information.

ABSTRACT OF THE DISCLOSURE

A code generation system is provided which optimizes a code generation for a control system applicable to an embedded control system without the need to increase its memory capacity. A total control unit 110 causes to read a model diagram and an operation diagram which depict a software specification stored in a memory 104, and starts specification analysis unit 106 to execute lexical and grammatical analyses thereof. Then, object-oriented function removing unit 107 is started to determine any function which is not used according to a function select item stored in the memory 104. Then, code generation unit 108 is started to generate a code on the basis of the result of the lexical and grammatical analyses of the software specification, and of an output code pattern determined by the object-oriented function removing unit 107.

FIG. 1

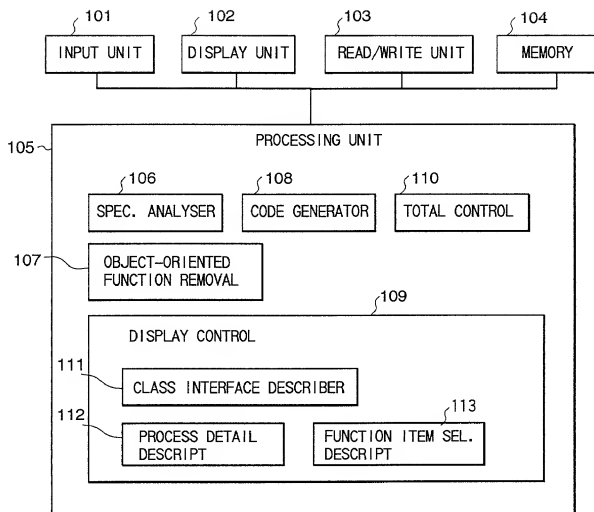


FIG. 2

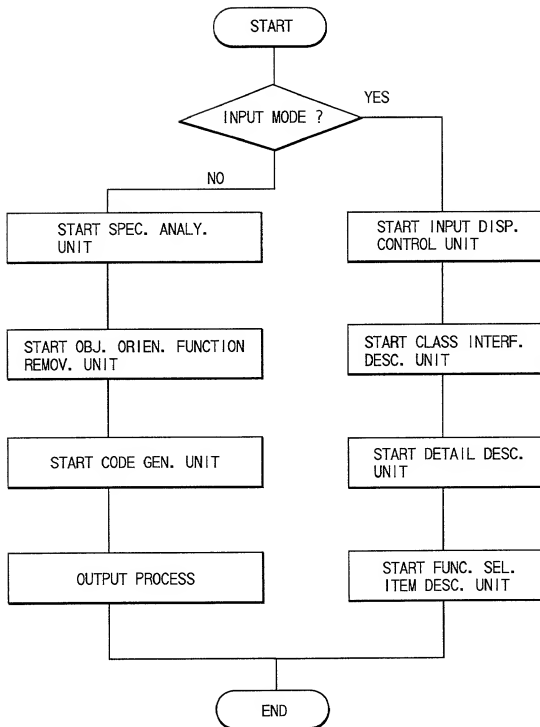


FIG. 3

SENSOR		301
A/D_VALUE	unsigned char	302
VALUE	signed short	
PREV_VALUE	signed short	
CHECK ()	boolean : void	303
UPDATE ()	void : void	

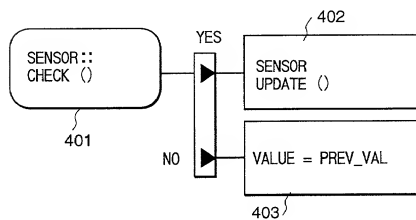
FIG. 4

FIG. 5

FUNCTION ITEM SELECTION <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>	
501 INPUT PATTERN	<div> OBJNAME METHODNAME (ARG) </div>
502 FUNC. ITEM TO USE	DYNAMIC GENER. OF INSTANCES
503 SET-UP OPTION	"TO USE" / "NOT TO USE"
504 OUTPUT CODE	TO USE DYNAM. GEN. OF INSTANCE OBJNAME. METHOD NAME (ARG) NOT TO USE DYNAM. GEN. OF INSTANCE OBJNAME_METHNAME (ARG)

FIG. 6

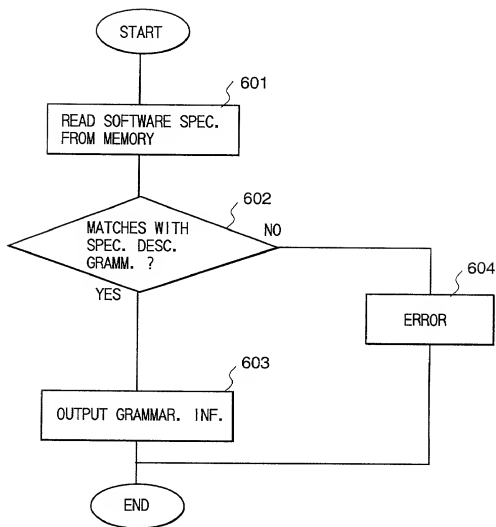


FIG. 7

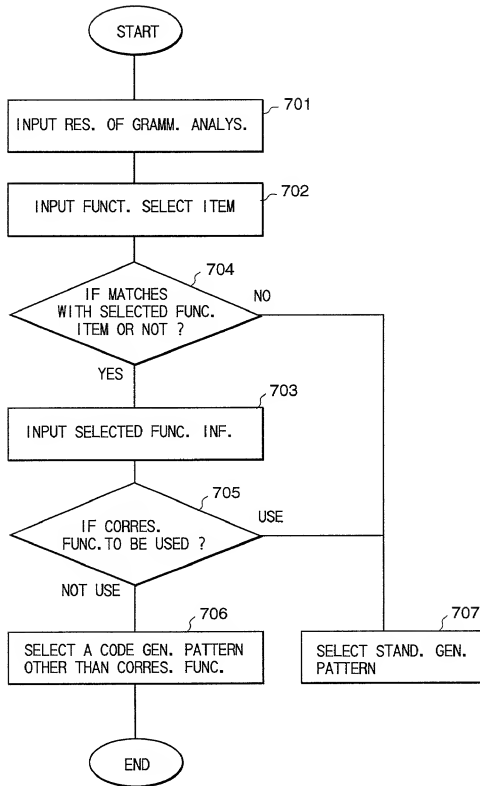


FIG. 8

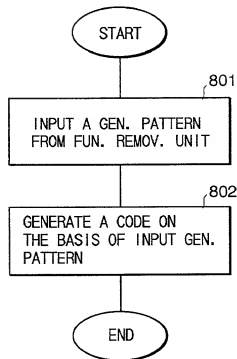


FIG. 10

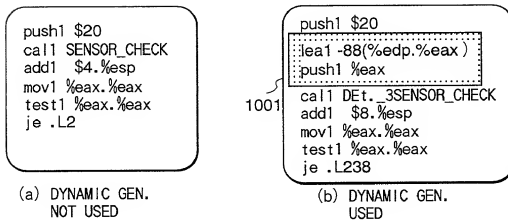


FIG. 9

```

if (SENSOR_CHECK {
    SENSOR_UPDATE():
} else {
    VALUE=PREV_VALUE): }

```

```

SENSOR_CHECK(void) {
    :
}

```

```

SENSOR_UPDATE(void) {
    :
}

```

(a) DYNAMIC GEN. FUNCTION
NOT USED. (C LANGUAGE)

```

if (SENSOR.CHECK()) {
    SENSOR_UPDATE():
} else {
    VALUE=PRE_VALUE): }

```

```

class SENSOR {
public:
    bool CHECK():
    void UPDATE():

```

```

SENSOR::CHECK(void) {
    :
}

```

```

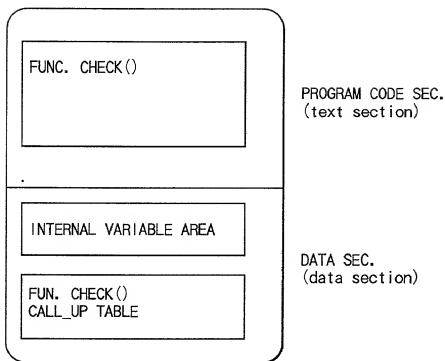
SENSOR::UPDATE(void) {
    :
}

```

(a) DYNAMIC GEN. FUNCTION
USED. (C++ LANGUAGE)

FIG. 11

(a) VIRTUAL FUNC. USED



(a) VIRTUAL FUNC. NOT USED

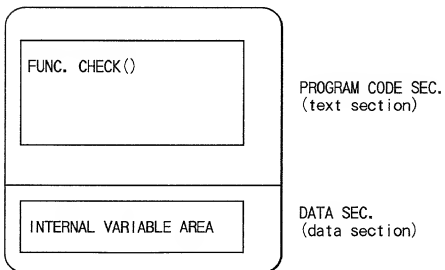


FIG. 12

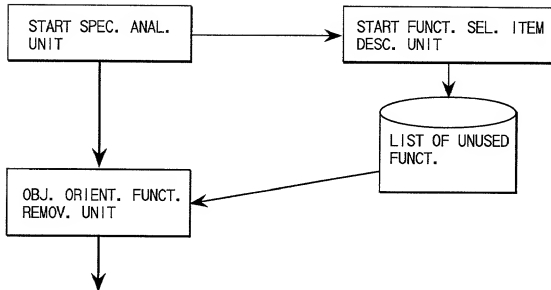


FIG. 13

FUNCT. REMOVING RULE: <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>	
RULE FOR SYSTEM A	
FUNCTION	SET - UP
1301 DYN. GEN. OF INSTANCE	<input type="radio"/> USE <input checked="" type="radio"/> NON USE 1303
1302 INHERITANCE	<input checked="" type="radio"/> USE <input type="radio"/> NON USE 1304

FIG. 14

FUNCTION	SET - UP
VIRTUAL FUNC.	NON USE
DYN. GEN. OF INSTANCE	USE
	:

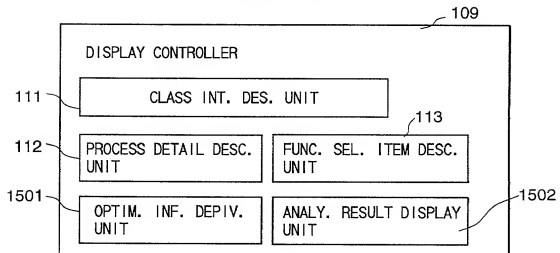
FIG. 15

FIG. 16

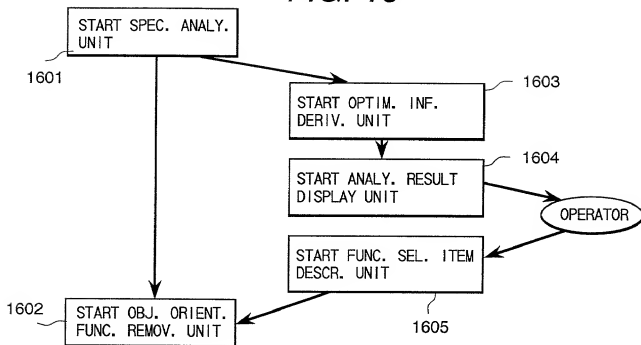


FIG. 17

(a) ANALYSIS RESULT DISPLAY UNIT

CLASS B	
NO	Nos. OF INSTANCES
PA	PARENT CLASS
CH	CHILDEN CLASS
IN	INHERIT. METHOD USED
	A::Meth 1()
	A::Meth 3()
	METHOD NOT USED
	B::Meth 5()

(b) USED FUNC. DESING. UNIT

OPTIMIZATION RULE	
FUNCTION	SET - UP
DYNAM. IMPLM.	<input type="radio"/> USE <input checked="" type="radio"/> NON USE
INHERITANCE	<input type="radio"/> USE <input checked="" type="radio"/> NON USE
	:
ROUTINE PROCESS NAME	Get() Set() ChkStatus()
:	:

DECLARATION AND POWER OF ATTORNEY - PATENT APPLICATION

As a below named inventor, I hereby declare that my citizenship, postal address and residence are as stated below; that I verily believe I am the original, first and sole inventor (if only one inventor is named below) or a joint inventor (if plural inventors are named below) of the invention entitled:

OBJECT-ORIENTED OPTIMIZATION CODE GENERATOR AND A METHOD THEREFOR

the specification of which

 X is attached hereto, or

 was filed on as Application Serial No. and
was amended on (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above. I acknowledge the duty to disclose all information known to be material to patentability as defined in 37 CFR §1.56. I hereby claim foreign priority benefits under Title 35, United States Code §119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)

Priority Claimed

<u>10-38329</u>	<u>Japan</u>	<u>20/02/1998</u>	<u>yes</u>
(Number)	(Country)	(Day/Month/Year)	
<u> </u>	<u> </u>	<u> </u>	<u> </u>
(Number)	(Country)	(Day/Month/Year)	

I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose all information known to be material to patentability as defined in 37 CFR §1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

<u>(Application Serial No.)</u>	<u>(Filing Date)</u>	<u>(Status)</u>
---------------------------------	----------------------	-----------------

I hereby appoint as principal attorneys Herbert I. Cantor, Reg. No. 24,392; James F. McKeown, Reg. No. 25,406; Donald D. Evenson, Reg. No. 26,160; Joseph D. Evans, Reg. No. 26,269; Gary W. Edwards, Reg. No. 31,824; and Jeffrey D. Sanok, Reg. No. 32,169, to prosecute and transact all business in the Patent and Trademark Office connected with this application and any related United States and international applications. Please direct all communications to:

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under §1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

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